

REMARKS

By this amendment, claims 1-23 have been amended. This amendment is made to even more clearly recite the claimed invention, does not add prohibited new matter and is fully supported by the specification (*see, e.g.*, page 2, line 28, to page 3, line 4 of the present specification). Reconsideration and withdrawal of the rejections set forth in the outstanding Office Action are respectfully requested in view of the following remarks.

Objections to the Title

The Office Action objects to the title, alleging that the title is not descriptive. Without agreeing with or acquiescing to the objections, Applicants note that the title has been amended to address this concern. Therefore, Applicants respectfully request that the Examiner withdraw the outstanding objection to the title.

Rejection under 35 U.S.C. § 102(e)

The Office Action rejects claims 1-23 under 35 U.S.C. § 102(e) as being anticipated by Yamazaki (U.S. Patent Application Publication No. 2003/0236947, hereinafter “YAMAZAKI”). Applicants respectfully submit that YAMAZAKI does not disclose, suggest, or render obvious all of the elements of the claimed invention.

Initially, Applicants note that the claims recite (using claim 1 as a non-limiting example):

An N-way set-associative cache memory comprising:

- a control register operable to indicate one or more ways among N ways;
 - a control unit operable to activate the way indicated by said control register; and
 - an updating unit operable to update contents of said control register,
- wherein the control register is configured to dynamically set the activated way based on a process executed by a processor; and
- wherein the control register prevents data from being evicted from the cache

memory by other tasks after task switching, and inhibits influence of the other tasks on hit ratio following task switching.

Applicants submit that YAMAZAKI fails to disclose, *inter alia*, an N-way set-associative cache memory “wherein the control register is configured to dynamically set the activated way based on a process executed by a processor; and wherein the control register prevents data from being evicted from the cache memory by other tasks after task switching, and inhibits influence of the other tasks on hit ratio following task switching,” as recited in independent claims 1 and 21. Furthermore, YAMAZAKI fails to appreciate the advantages of the aforementioned features recited in the claims. Thus, Applicants submit that YAMAZAKI fails to disclose each and every feature of the claimed invention, as required under 35 U.S.C. § 102, and respectfully request that the Examiner withdraw the outstanding rejections.

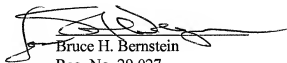
Lastly, dependent claims 2-20 and 21-23 are submitted to recite further patentable subject matter of the invention and, therefore, are also submitted to be allowable over the prior art of record. As such, allowance of the dependent claims is deemed proper for at least the same reasons noted above for the independent claims, in addition to reasons related to their own recitations. Accordingly, Applicant respectfully requests reconsideration of the outstanding rejections and an indication of the allowability of all of the claims in the present application.

CONCLUSION

In view of the foregoing amendments and remarks, Applicants submit that all of the claims are patentably distinct from the prior art of record and are in condition for allowance. The Examiner is respectfully requested to pass the above application to issue.

Please charge any deficiencies in fees and credit any overpayment of fees to Deposit Account No. 19-0089. Should the Examiner have any questions or comments, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,
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